

FORM PTO-1449/A and B (Modified) INFORMATION DISCLOSURE STATEMENT BY APPLICANT				APPLICATION NO.: Unassigned		AT DCKET NO.: S1022.81223US00	
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				APPLICANT: Laurent REGNIER			
				GROUP ART UNIT: Unassigned		EXAMINER: Unassigned	
Sheet	1	of	1				

U.S. PATENT DOCUMENTS

Examiner's Initials	Cite No.	U.S. Patent Document		Name of Patentee or Applicant of Cited Document	Date of Publication or of issue of Cited Document MM-DD-YYYY
		Number	Kind Code		
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		5,621,663		Skagerling	04/15/1997
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Examiner's Initials	Cite No.	Foreign Patent Document			Name of Patentee or Applicant of Cited Document (not necessary)	Date of Publication of Cited Document MM-DD-YYYY	Translation (Y/N)
		Office/ Country	Number	Kind Code			
		EP	1 184 790	A2	Mann	03/06/2002	

OTHER ART — NON PATENT LITERATURE DOCUMENTS

Examiner's Initials	Cite No.	Include name of the author (in CAPITAL LETTERS) title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, relevant page(s), volume-issue number(s), publisher, city and/or country where published.	Translation (Y/N)
		International Search report from corresponding Intn'l Application No. PCT/FR02/03724, filed October 29, 2002	
		International Search report from related Intn'l Application No. PCT/FR02/03723, filed October 29, 2002	
		International Search report from related Intn'l Application No. PCT/FR02/03526, filed October 15, 2002	
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		Plauser, D., <u>Real-Time Unix: Timing is Everything</u> , Mini Micro Systems, Cahners Publishing CY, Boston, vol. 22, no. 2, February 1, 1989, pages 72-76, XP000039292	
/JP/		Okeeffe, H. et al., <u>Einheitliche Debug-Schnittstelle Der Zukunft Der Nexus-Standard Soll Die Kommunikation Zwischen Emulator Und Prozessor Vereinheitlichen</u> , Elektronik, Franzis Verlag GMBH, Munchen, vol. 49, no. 18, Sept. 5, 2000, pages 124-128, XP001107530	
/JP/		Macnamee, C., <u>Ein Interface Fuer Alle Nexus Als Globaler Debug-Standard</u> , Elektronikpraxis, Vogel, Wuerzburg, no. 3, Feb. 8, 2000, pages 104-108, XP008001346	
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		Nexis 5001 Forum: <u>Standard for a Global Embeded Processor Debug Interface</u> , Dec, 15, 1999, IEEE-ISTO XP002247195	

EXAMINER: /Jacob Petranek/	DATE CONSIDERED: 12/12/2007
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#EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to Applicant.

*a copy of this reference is not provided as it was previously cited by or submitted to the office in a prior application, Serial No. __, filed __, and relied upon for an earlier filing date under 35 U.S.C. 120 (continuation, continuation-in-part, and divisional applications).

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